



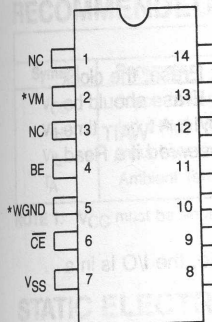
52801 256-BIT (16 X 16) EEPROM

- Electrically Erasable PROM
- Advanced SNOS N-Channel Technology
- 5 Volt Only Operation
- In-System Reprogrammability
- Memory Margining Capability
- Fully TTL Compatible
- Serial I/O
- Unlimited Read Accesses
- 125 kHz Maximum Clock Rate

The NCR 52801 is a 256 bit Electrically Erasable PROM utilizing the Silicon-Nitride-Oxide-Silicon (SNOS) process developed by NCR. Designed for ease of use, the 52801 requires only a 5 volt source for all modes of operation. All voltages required for programming are generated internally by an on-chip bias generator, thus eliminating the need for external high voltage supplies. The timing of the 52801 is controlled totally by external TTL logic level signals. Address and data flow are in serial format through a common TTL compatible I/O port at a maximum clock rate of 125 kHz. Reprogramming the 52801 is basically a two step process, first erasing either a word or the entire block, and then writing a new word. The 52801 is available in a 14 pin package, in both commercial and industrial versions.

MEMORIES
EEPROM

PIN CONFIGURATION



FUNCTIONAL BLOCK DIAGRAM

